

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Uzi Vishkin : Art
 Serial No: 10/529,310 : Unit # 2122
 Filed: 25 March 2005 : Examiner:
 Title: OPTICAL INTERCONNECT STRUCTURE : Unknown
 IN A COMPUTER SYSTEM AND METHOD
 OF TRANSPORTING DATA BETWEEN :
 PROCESSING ELEMENTS AND MEMORY :
 THROUGH THE OPTICAL :
 INTERCONNECT STRUCTURE

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

The Applicant wishes to make the following art references of record in the above-identified Patent Application pursuant to 37 C.F.R. §§ 1.97 and 1.98, and to the Duty of Disclosure set forth in 37 C.F.R. § 1.56

Although the information submitted herewith may be "material" to the Examiner's consideration of the subject Patent Application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search was made or that no other material information, as defined in 37 C.F.R. § 1.56(b), exists.

The cited references are:

I. Patent references

<u>Ref. No.</u>	<u>Patent No.</u>	<u>Issue Date</u>	<u>Inventor(s)</u>
A	6,542,918	4/1/2003	Viskin
B	5,781,465	7/14/1998	Lutz, et al.
C	4,922,415	5/1/1990	Hemdel
D	4,700,325	10/13/1987	Ware

II. Other art references

<u>Reference No.</u>	<u>Description</u>
A1	Neil Savage, "Linking With Light", IEEE Spectrum, Vol. 39, Issue 8, August 2002, Pages 32-36. http://www.usc.edu/dept/engineering/eleceng/Adv_Network_Tech/Html/publications/IEEESpectrum.8.8.02.pdf
B1	D. Naishlos, et al., "Towards a First Vertical Prototyping of an Extremely Fine-Grained Parallel Programming Approach", Theory of Computer Systems, 36 (2003), 521-552 (Special Issue of SPAA2001) www.cs.umd.edu/projects/cosmic/papers/xmt-spaa01.ps
C1	U. Viskin, "Explicit Multi-Threading (XMT): A Pram-On-Chip Vision", http://www.umiacs.umd.edu/users/vishkin/XMT/

MR2833-39
Serial No.: 10/529,310

This Information Disclosure Statement is being filed more than three months subsequent to the filing date of the subject Patent Application, but before the mailing of a first Office Action.

A Form PTO-1449 and copies of the referenced publications are submitted along with this document. It is requested that the Examiner consider the cited references and make them of record in the above-referenced Patent Application.

Respectfully submitted,
FOR: ROSENBERG, KLEIN & LEE



David I. Klein
Registration #33,253

Dated: 18 Nov. 2005

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Application Number	10/529,310
Filing Date	03/25/2005
First Named Inventor	U. Vishkin
Art Unit	2122
Examiner Name	
Attorney Docket Number	MR2833-39

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Examiner Signature		Date Considered	
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Sheet	2	of	2
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Complete if Known

Application Number	10/529.310
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Filing Date	03/25/2005
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First Named Inventor	U. Vishkin
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Art Unit	2122
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Examiner Name

Attorney Docket Number	MR2833-39
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NON PATENT LITERATURE DOCUMENTS

[illegible]

Examiner Signature	/Kidest Bahta/	Date Considered	04/19/2008
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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /K.B./